

Patent Abstracts of Japan

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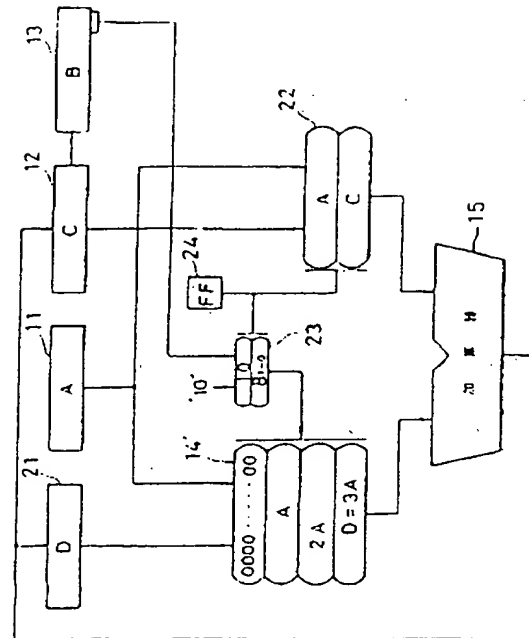
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TITLE : MULTIPLICATION SYSTEM



ABSTRACT : PURPOSE: To execute multiplication at a high speed by calculating the partial product between two bits of multiplier data and multiplicand data by one addition of an adder.

CONSTITUTION: A selector 22 selects multiplicand data A held in a register 11 or data C held in a register 12 and supplies selected data to one input of an adder 15. A selector 23 selects lower two-bit data B1-0 of multiplier data B held in a register 13 or data '10' and supplies selected data to a selector 14'. The selector 14' selects one data from data '0 to 0', multiplicand data A, two-fold value data 1A of multiplicand data A, and three-fold value data D(=3A) of multiplicand data A in accordance with these selection and supplies it to the other input of the adder 15. Since the partial product between two bits of multiplier data and multiplicand is obtained by one operation, multiplication is executed at a high speed.

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